**DAILY ASSESSMENT FORMAT**

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| **Date:** | **28-05-2020** | **Name:** | **Akshata Madiwalar** |
| **Course:** | **Logic Design** | **USN:** | **4AL17EC046** |
| **Topic:** | **Boolean equations for digital circuits. Combinational circuits: Conversion of MUX and Decoders to logic gates.**  **decoder with common design of 7 segment anode display** | **Semester & Section:** | **6th sem & A sec** |
| **Github Repository:** | **Akshata-course** |  |  |

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| **FORENOON SESSION DETAILS** |
| **Report – Report can be typed or hand written for up to two pages** |
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| **Date:** | **28-05-2020** | **Name:** | **Akshata Madiwalar** | |
| **Course:** | **Python** | **USN:** | **4AL17EC046** | |
| **Topic:** | **Application 5: Build a desktop database application.** | **Semester & Section:** | **6th sem & A sec** | |
| **AFTERNOON SESSION DETAILS** | | | |
| **Report – Report can be typed or hand written for up to two pages.** | | | |
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